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MEARS Technologies to Present at Microprocessor Forum 2007

Paper to focus on means of minimizing microprocessor gate leakage and improving drive current.

San Jose, Calif., May 22, 2007 – Robert J. Mears, president and chief technical officer of MEARS Technologies, will be a featured speaker at Microprocessor Forum 2007, where he will deliver a paper titled, “New Semiconductor Technology Enhances Drive Current, Lowers Leakage.” The paper will describe a method of device scaling below 45nm that incorporates a new technique to mitigate microprocessor gate leakage and enhance drive current through insertion of a high-mobility epitaxial silicon-channel replacement layer, which involves no new elements in the fabrication process.

“We are very excited about presenting at this year’s Microprocessor Forum to share MEARS Technologies’ view as to how the microprocessor and semiconductor industry-at-large can better address one of their biggest challenges,” said Dr. Mears. “We look forward to providing more in-depth technical information about this innovative new technology and how it is helping IC makers make best use of their manufacturing design resources by lowering leakage and increasing mobility.”

Max Baron, In-Stat principal analyst and the content editor for this year’s Fall Microprocessor Forum, said, “We are pleased to have MEARS Technologies presenting one this year’s highly anticipated papers. Microprocessor Forum gathers together a sophisticated engineering audience from around the globe and they won’t be disappointed given the number of high caliber companies participating.”

Microprocessor Forum will take place May 22 – 23, 2007 in San Jose, California at the Doubletree Hotel.

About MEARS Technologies

MEARS Technologies is an emerging materials technology company that provides advanced silicon processes and engineering services to semiconductor device manufacturers and contract foundries. The company combines a core competency in materials engineering and quantum mechanics with practical semiconductor process technology know-how to optimize the power efficiency and performance of integrated circuits manufactured on deep sub-micron processes. With a licensing model and strong patent position covering breakthrough silicon structures, methods and processes, MEARS

Technologies enhances the fundamental electronic properties of silicon without requiring new manufacturing equipment or the use of exotic materials.

About Microprocessor Forum

Microprocessor Forum is configured to both educate engineers and showcase the latest product announcements. Every presentation made at Microprocessor Forum describes challenges encountered by chip designers and outlines the real world solutions that were implemented to overcome those challenges. Every session is developed to address the primary concerns of OEM and SoC designers today. Presentations are solicited and hand picked by In-Stat's analysts to provide marketing-free, quantitative technical information geared toward both educating engineers and showcasing the latest product announcements. In its 19th year, Microprocessor forum delivers on technical content, unprecedented networking and real world solutions to the challenges that face today's design engineers.

In-Stat is proud to present Microprocessor Forum 2007 – check it out online at:
<http://www.instat.com/mpf/07/index.htm>

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